## **INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

**PTO Form 1449** 

Attorney Docket No. 053785-5157	Serial No. Unassigned	
Applicants Young-Joo KIM		
Filing Date	Group	
November 21, 2003	Unassigned	

### U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date
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	FOREIGN PA	TENT DOCUMENTS				
Document Number	Date	Country	Class	Sub Class	Trans YES	NO NO

	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
TSP	H. IKEDA. "Evaluation of Grain Boundary Trap States in Polycrystalline-Silicon Thin-Film Transistors by Mobility and Capacitance Measurements." <u>Journal of Applied Physics</u> . Vol. 91, No. 7, April 2002, pp. 4637-4645.

Examiner		Date Considered	11/23/05
Examiner:	Initial if reference considered, wh	nether or not citation is in conforman	ce with MPEP 609; draw line through
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# INFORMATION DISCLOSURE CITATION

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PTO Form 1449

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Application No.

053785-5157

10/717,676

Applicant: Young-Joo KIM

Filing Date: November 21, 2003

Group Art Unit: Unassigned

#### U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Sub Class	Filing Date	
FOREIGN PAT	ENT	DOCUMENTS						
		Document Number	Date	Country	Class	Sub Class	Translation YES NO	
758		2001-0087667	09/21/2001	Korea			Abstract	
758 758		2001-0066251	07/11/2001	Korea			Abstract	
OTHER DOC	JMEN	TS (Including Auth	or, Title, Date, Pert	tinent Pages, Etc.)				
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March 9, 2005

Page 1 of 1



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Applicant: Young-Joo KIM

Group Art Unit: Unassigned

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#### U.S. PATENT DOCUMENTS

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TSP			np. Proc. Vol. 542,					,
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February 19, 2004

Page 1 of 1